

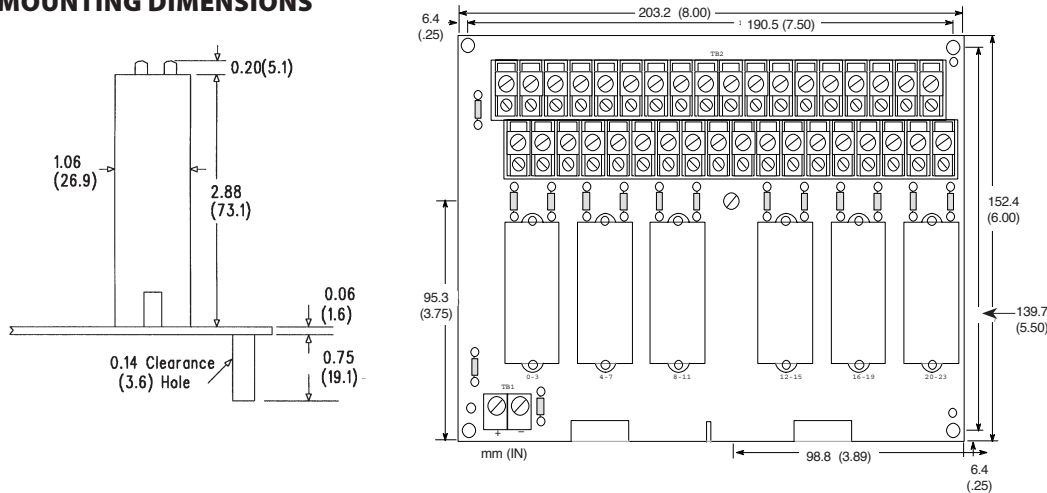
The 1781-A6QA mounting board can be used with 6 1781-series quad modules (24 I/O points). A total of 4 points within each quad module share 2 common connections on the mounting board terminal strip. The barrier strip will accept ring or spade lugs, insulated wire with stripped ends, or ferrules. The power side allows each module to have a shared common for only that module, and each module is isolated from remaining modules. The signal side has a common logic supply bus (+Vcc and dc return) shared with each of the modules. Specify 1781-CxEx Cable Assembly. DIN mounting is not available.

Power Side			Signal Side		Power Side			Signal Side		Power Side			Signal Side	
Bit	Pos.	Neg.	Signal	DC Return	Bit	Pos.	Neg.	Signal	DC Return	Bit	Pos.	Neg.	Signal	DC Return
Bit 0	1-4 COM	1	47	48	Bit 8	9-12 COM	9	31	32	Bit 16	17-20 COM	17	15	16
Bit 1	1-4 COM	2	45	46	Bit 9	9-12 COM	10	29	30	Bit 17	17-20 COM	18	13	14
Bit 2	1-4 COM	3	43	44	Bit 10	9-12 COM	11	27	28	Bit 18	17-20 COM	19	11	12
Bit 3	1-4 COM	4	41	42	Bit 11	9-12 COM	12	25	26	Bit 19	17-20 COM	20	9	10
Bit 4	5-6 COM	5	39	40	Bit 12	13-16 COM	13	23	24	Bit 20	21-24 COM	21	7	8
Bit 5	5-6 COM	6	37	38	Bit 13	13-16 COM	14	21	22	Bit 21	21-24 COM	22	5	6
Bit 6	5-6 COM	7	35	36	Bit 14	13-16 COM	15	19	20	Bit 22	21-24 COM	23	3	4
Bit 7	5-6 COM	8	33	34	Bit 15	13-16 COM	16	17	18	Bit 23	21-24 COM	24	1	2

NOTES:

- Logic supply +Vcc (+5 V dc) and dc return is supplied through the 2 terminal, j20 logic-supply connector, marked with + or -.
- Logic supply dc return is connected to all even pins of the 50-pin edge card connector
- Logic-side (signal) connector pin numbers are for 50-pin connector.
- Power-side terminals are polarized for dc applications, and non-polarized for ac applications.
- The backplane is shipped with a jumper configuration to supply the logic voltage through pin 49 of the edge connector
- The signal pin is pulled up to +Vcc when not asserted through a module's internal pull-up resistor, and pulled down to dc return when asserted.

MOUNTING DIMENSIONS



SCHEMATIC DIAGRAM

